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PATENT NUMBER and
ISSUE DATE

3

U.S. UTILITY Patent Application

APPL NUM 10082294	FILING DATE 02/26/2002	CLASS 327	SUBCLASS 21-A	GAU 3843	EXAMINER L3C
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**APPLICANTS: Hoshide Takahiro;

**CONTINUING DATA VERIFIED:

** FOREIGN APPLICATIONS VERIFIED:

JAPAN 2001-390151 12/21/2001

PG-PUB <input type="checkbox"/> DO NOT PUBLISH <input type="checkbox"/>	RESCIND <input type="checkbox"/>	ATTORNEY DOCKET NO 027260-516
Foreign priority claimed <input type="checkbox"/> yes <input type="checkbox"/> no 35 USC 119 conditions met <input type="checkbox"/> yes <input type="checkbox"/> no Verified and Acknowledged Examiners's initials		
TITLE : DC bias control circuit, optical receiver, and DC bias control method		

U.S. DEPT. OF COM. (PAT & TM) PTO-436 (Rev. 12-94)

NOTICE OF ALLOWANCE MAILED		CLAIMS ALLOWED	
		Assistant Examiner	Total Claims Print Claim for O.S.
ISSUE FEE		Primary Examiner	DRAWING
Amount Due	Date Paid		Sheets Drwg. Figs. Drwg. Print Fig.
<input type="checkbox"/> TERMINAL DISCLAIMER		PREPARED FOR ISSUE	Application Examiner
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